

Power dissipation for PetiRoc with integrated TDC

Channel	Chamber	FPGA+GBT	1 station	1 YE3	Rack Power	PetiRoc Dissipation
	640	20 + 1W	18		Eff 66%	
[mW]	[watt]	[W]	[W]	[W]	[W]	[W]
3.6	2.304	21	419	839	432	1271

One Endcap Power

1271