Overview

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Resistive Plate Chambers

2.1 Technical co-ordination (Barrel)

The major technical co-ordination responsibility is assigned to the Bari group:

Overall VLSI FE boards Mechanics Integration Assembly and test Data base Test beam G. Iaselli F. Loddo A. Ranieri A. Clemente, A. Ranieri/A. Clemente S. Nuzzo/A. Colaleo M. Abbrescia/G. Zito A. Colaleo

Moreover:

BakeliteP. Vitulo/G. Belli (Pavia)VLSI quality controlP. Vitulo/Torre (Pavia)FE boards quality controlP. Vitulo/Torre (Pavia)RB1 mechanicsYe (PU)RB2,RB3, RB4 mechanicsV. Genchev/L. Litov (Sofia)RB1 assembly and testY. Ye (PU)RB3 assembly and testV. Genchev/L. Litov (Sofia)

2.2 Technical co-ordination (Forwad)

The major technical co-ordination responsibility is now assigned to the CMS RPC co-ordinator with the help of local co-ordinators

Overall Bakelite Gaps VLSI FE boards Mechanics Integration G. Iaselli, S.J. Park, H.Horani, Y.Ye P. Vitulo/G. Belli (Pavia) Korea F. Loddo (Bari) Pakistan Korea/Pakistan Korea/Pakistan

Moreover:

RB1 mechanics RB2,RB3, RB4 mechanics RB1 assembly and test RB3 assembly and test Y. Ye (PU) V. Genchev/L. Litov (Sofia) Y. Ye (PU) V. Genchev/L. Litov (Sofia)



3.1 Progress in the barrel Italy





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3.2 Progress in the forward Pakistan

Pakistan Production Site Status

- Building ready end of October 2000.
 - Cost PKR 5 million (~ 100 kUSD)
- Mechanical Workshop
 - Lathes (2, Milling (1), Drilling, Fitter benches
 - Cost PKR 2 million (~ 40 kUSD)

Schedule and Planning

- Total of 432 chambers for RE2/3/4.
- 10 deg. Chamber, covering low eta region.
- 10% contingency. Total number of chambers 475.
- One chamber per day.
- For all chambers required assembly time is:

X + **104 weeks** "X" is the start time.

3.2 Progress in the forward Pakistan

Assembly Plans

- Input Quality Assurance: (Gaps)
 - Visual Inspection
 - Dimensional Check
 - Gas Leak
 - Leakage Current
- Assembly Stages:
 - Sandwich
 - Services + Mechanics
 - Mounting of FEB and initial testing

Chamber Testing

- Gas System in place
 - 10 channels, 4 gases, digital mass flow meters, Cost (13.5 kCHF)
- Gases are ordered (R134, Iso-butane, SF6)
 Cost (20 kCHF), available 15.10.2000
- DAQ and HV system from CAEN
 Cost (100 kUSD) yet to be ordered.
- Slow Control Monitoring. Software and hardware has been ordered.
 - Cost 30 kCHF

3.2 Progress in the forward Pakistan

Electronics (FEBs)

- We have identified a company which can:
 - manufacture four layer PCBs
 - populate SMDs
 - test the FEBs
- Once the design of FEBs is finalized:
 - For complete production 9 months are needed.
 - About 15,000 ASICs will be required for the full endcap RPC system.

Concerns

- Following items can affect the schedule and planning:
 - Delivery of gaps from Korea.
 - Supply of ASICs for FEBs from Italy.
 - Strips from Korea.
 - HV Connector design and production.
- Clear definitions of responsibilities:
 - Gas and gas pipes
 - Cooling
 - Connection to linkboards
 - LV, HV supply systems

Status of the Gap Production Tools

- Prototype facilities prepared
- 1) Glue dispensing tools
- 2) Silk screen facility for graphite coating
- 3) Mylar and copper sheet coating facility
- 2. Prototype facilities currently being constructed
 - 1) Gap storage facility
 - 2) Bakelite storage facility
 - 3) Gap assembly facilities
 - * Gap assembly tables : 5 optical tables
 - * Glue hardening storage shelf with uniform pressurization tubes
 - * Gap & spacer jigs (need the final gas gap design results)

First set of the prototype facilities -> End of Oct. 2000



Silk Screen Facility



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Schedule for the Production of Gas gaps

- 1. Debugging of the prototype facilities
 - 1) Nov. 1, 00 Feb. 28, 01 :
 - * Mechanical tests
 - * Production of 1 set of gaps for RE1/1, RE2/1, RE2/2, each
 - * Review of the facility designs
 - * Debugging
- 2. Mar. 1, 01 Aug. 31 : Construction of the main facilities

3. Real productions

1) Mar. 1, 01 – Aug. 31, 01 : First 20 sets of gas gaps

(with one set of facilities)

- 2) Sep. 1, 01 Dec. 31, 01 : 40 sets of gaps (with two sets of facilities)
- 3) Jan. 3, 02 Dec. 31, 02 : 500 sets of gaps
- 4) Jan. 3, 03 ? : 400 sets of gaps

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Status of the Assembly Site for Highest eta Stations

Common area for the assembly and gap production

- -> Reduction of the cost
- -> Safety reason

Facilities in preparation

- 1) Assembly tables : 5
- 2) DAQ system for 256 channel read-out

Facilities prepared

Assembly area
 Basic machining tools & assembly tables
 Testing lab. Equipped with DAQ system
 6 channel gas feeding facility

Production schedule

- 1) Mar. 1, 00 Sep. 30, 00 : first 8 RPCs for RE2/1 and RE1/1
- 2) Oct. 1, 00 Jan. 31, 02 : 20 RPCs
- 3) Feb. 1, 02 Dec. 31, 02 : 100 RPCs
- 4) Jan. 1, 02 ? : 80 RPCs

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4.1 Electronic system

ESR for VLSI and PCB held on the 13th September

Front End Chip SpecificationsTools for automatic testsTest of low volume production

•Front End Board Layout

- •Board components
- •Latch-up protection
- •Slow control interface
- •Kapton design
- •Irradiation test
- •Production plans

ESR documentation available at:

http://cmsdoc.cern.ch/cms/archives/00/MU_ESR/main.html

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4.2 Electronic system





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7.2 Items on the critical path Forward

The FW project is one more that one year behind the BL one

Difficult to define item on the critical path before the design is complete and the schedule understood

EDR in January 2001

Barrel know-out can help a lot

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9. Sub-system integration Barrel/Forward

Connection to link board: define link board position and cable routes

Ground scheme very preliminary

Cooling system: workshop in November

RE1 integration scheme to be better defined

10. Areas of concern

Barrel groups are small with no strong administrative support. Technical stuff below threshold (by 30%)

Forward groups have limited contact among themselves. Most of the work so far has been done at CERN. Improve the home laboratories capabilities.

Transport of large gaps (Barrel) from Italy to PU and transport of chambers from PU to CERN may be costly and painful

BL and FW Integration schemes unsatisfactory. Work needed. Help from CERN needed